JAN 2 7 2006 W

INFORMATION
DISCLOSURE
CITATION
PTO-1449

Atty. Docket No. 960637D	Serial No. 10/708,145

Applicant(s): OKAJIMA, Yoshinori

Filing Date: February 11, 2004

Group Art Unit: 2816

## U.S. PATENT DOCUMENTS

Examiner Initial	*	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
De	AA AB	5,245,231	Kocis et al.	09-14-1993	3 <del>07</del> 327	<del>606</del> 277	12-30-1991
	AC						
	AD				•		
	AE			·			

## FOREIGN PATENT DOCUMENTS

	Document No.	·	Date	Country	Translation (Yes or No)
 AF					
 AG				•	
 AH					•
 AI					
 AJ					

**OTHER DOCUMENTS** 

	AK_	Copy of Japanese Office Action dated January 10, 2006, 2 pages.			
Cre	AL	Timo Rahkonen et al.: "The Use of Stabilized CMOS Delay Lines in the Digitization of Short Time Intervals," IEEE International Symposium on June 16, 1991, vol. 4, pp. 2252-2255.			
Examiner	) as	Bornalia Corpate Considered May 14, 2006			